

Electronic Acknowledgement Receipt

EFS ID:	1536551
Application Number:	10748906
International Application Number:	
Confirmation Number:	7419
Title of Invention:	Semiconductor memory device testable with a single data rate and/or dual data rate pattern in a merged data input/output pin test mode
First Named Inventor/Applicant Name:	Jong-Cheol Lee
Customer Number:	22150
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Attorney Docket Number:	8836-223 (ID12244-US)
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1	Amendment - After Non-Final Rejection	AmdReOAdtd112406-8836-223.pdf	906018	no	9

Warnings:

Information:					
2	Drawings	replaceSheet-8836-223.pdf	130691	no	1
Warnings:					
Information:					
Total Files Size (in bytes):			1036709		
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